

DazzleStar Disassembler:

DZ.COM uses RST 2, RST 3 and RST 4 instructions for fast jumps to keep the screen updated:

<u>RST 2:(0010H)</u>	<u>RST 3:(0018H)</u>	<u>RST 4:(0020H)</u>
PUSH IX	JMP 8C87H	JMP 8CA6H
POP D		
DADD		
RET		

For Heath computers, this won't work since RST 3 is already in use for the keyboard interrupt. The solution appears to be to use RST 5 (0028H) instead.

When executed, an RST instruction will push the PC to the stack and then execute the code at the RST address. RST 2 will execute and return to the instruction following the RST 2 command. RST 3 and 4 will jump to the specified address and branch according to the program code and conditions and return to the instruction following the RST n instruction when a RET pops the value pushed when the RST was encountered.

When first started, the first command is a jump to the routine to test the program and equipment status. The CPU must be a Z-80. The program will check the code by performing a simple checksum of all bytes between 0300H and 8FB6H and comparing the result to the stored value at 8FB7 and 8FB8. It then checks location of the BDOS to determine if the TPA top is less than 38400k. It will exit to CP/M if any test fails. If all checks are good, the program will relocate almost all code and set up the program's work space around 9000H to the top of TPA.

Revising the program to use RST 5 instead of RST 3 will change the checksum and the stored value must be updated to the new sum. This can be accomplished by appending "CKSUM.HEX". This will read the modified DZ-HEATH.COM (DZ.COM configured for Heath H-19 terminal) into memory, compute the new checksum, replace the stored checksum value and exit to CP/M. The revised program is still in memory and can be saved with the command "SAVE 143 filename.ext".

CKSUM.Z80

(Assemble to a HEX file for appending with DDTZ.COM.)

```
; Code to be added to the end of the revised DZ using DDTZ.  
; A jump to this routine will perform the simple  
; sixteen-bit sum of all bytes between 0300h and 8FB6h and  
; will install at location 8FB7h of the revised program.  
; Immediately 'save 143 filename.ext' or peek the memory  
; location and edit the file to change the value.  
;
```

```
.Z80  
ASEG  
ORG 9000H
```

```

        LD    B,00H        ;clear registers for use
        LD    HL,0000
        LD    DE,0103H    ;start address in DE
LOOP:EX DE,HL
        LD    C,(HL)      ;load byte to C
        INC  HL           ;inc to next byte
        EX   DE,HL       ;address in DE, sum in HL
        ADD  HL,BC       ;add byte to sum
        LD   A,07FH      ;end low byte to A
        CP   E           ;match?
        JR   NZ,LOOP     ;no
        ;
        LD   A,8AH      ;end high byte to A
        CP   D           ;on last page?
        JR   NZ,LOOP     ;no
        ;
        EX   DE,HL      ;HL is now at B7h
        LD   (HL),E     ;write low byte
        INC  HL
        LD   (HL),D     ;write high byte
        CALL 0000       ;exit to CP/M
        END

```

Using the ZCPR 3.4 (NZCOM) intrinsic PEEK command will display memory in half page blocks. At the CP/M prompt, typing P 0100 will display half of the 01 page. Typing P will display the following half page, etc.

What I have found is that the program relocates most of the code to areas other than where it resides when loaded for disassembly. If we run the program and immediately exit back to CP/M, we can save the memory contents of the 'run-time' environment to a file. This can now be disassembled to see how the program actually works. By generating a relocation map for the 'DF' bytes, the original file can be loaded and patched where needed.

The relocation code follows:

DazzleStar -- Program Code Relocation (Ancient form of copy protection!)

```

; Routine for Code Moves
;
8EEE 4E   J$8EEE:   LD    C,(HL)    ; Source in BC
8EEF 23   I$8EEF:   INC   HL
8EF0 46   D.8EF0:   LD    B,(HL)
8EF1 23           INC   HL    ; pointer to next data
;
;
; Subroutine _____
; Inputs _____
; Outputs _____
;
8EF2 C5   C$8EF2:   PUSH BC    ; BC to stack
8EF3 5E           LD    E,(HL) ; LD DE
8EF4 23           INC   HL
8EF5 56           LD    D,(HL)
8EF6 23           INC   HL    ; pointer to next data
8EF7 AF           XOR   A
8EF8 BB           CP    E
8EF9 2006        JR    NZ,J.8F01 ; zero when moves done
;
8EFB BA           CP    D
8EFC 2003        JR    NZ,J.8F01
;
8EFE E1           POP   HL
8EFF D5           PUSH DE
8F00 E9           JP    (HL)    ; start running program
;
; -----
8F01 4E   J.8F01:   LD    C,(HL)    ; LD BC
8F02 23           INC   HL
8F03 46           LD    B,(HL)
8F04 23           INC   HL    ; HL = next moves data
8F05 E3           EX   (SP),HL   ; source in HL, move addr ptr on stack
8F06 EDB8        LDDR
8F08 E1           POP   HL    ; get saved move addr pointer in HL
8F09 18E3        JR    J$8EEE
;
; Data for moves 2 through xx using LDDR
; S = Source, D = Destination, C = Count
; ----- Usage
8F0B 7B8E        ?.8F0B:   DEFW 8E7BH     S
8F0D FF93        DEFW 93FFH     D
8F0F 0100        DEFW 0001H     C
8F11 FF93        DEFW 93FFH     S
8F13 FE93        DEFW 93FEH     D
8F15 BC00        DEFW 00BCH     C
8F17 7A8E        DEFW 8E7AH     S

```

8F19 4293	DEFW9342H	D
8F1B 8101	DEFW0181H	C
8F1D C291	DEFW91C2H	S
8F1F C191	DEFW91C1H	D
8F21 0B00	DEFW000BH	C
8F23 F98C	DEFW8CF9H	S
8F25 B691	DEFW91B6H	D
8F27 4303	DEFW0343H	C
8F29 748E	DEFW8E74H	S
8F2B 738E	DEFW8E73H	D
8F2D 6200	DEFW0062H	C
8F2F B689	DEFW89B6H	S
8F31 118E	DEFW8E11H	D
8F33 B10D	DEFW0DB1H	C
8F35 6180	DEFW8061H	S
8F37 6080	DEFW8060H	D
8F39 1100	DEFW0011H	C
8F3B 057C	DEFW7C05H	S
8F3D 4F80	DEFW804FH	D
8F3F 140A	DEFW0A14H	C
8F41 3C76	DEFW763CH	S
8F43 3B76	DEFW763BH	D
8F45 0F00	DEFW000FH	C
8F47 F171	DEFW71F1H	S
8F49 2C76	DEFW762CH	D
8F4B 9D02	DEFW029DH	C
8F4D 9073	DEFW7390H	S
8F4F 8F73	DEFW738FH	D
8F51 1400	DEFW0014H	C
8F53 546F	DEFW6F54H	S
8F55 7B73	DEFW737BH	D
8F57 2904	DEFW0429H	C
8F59 536F	DEFW6F53H	S
8F5B 526F	DEFW6F52H	D
8F5D 0C00	DEFW000CH	C
8F5F 2B6B	DEFW6B2BH	S
8F61 466F	DEFW6F46H	D
8F63 3401	DEFW0134H	C
8F65 136E	DEFW6E13H	S
8F67 126E	DEFW6E12H	D
8F69 2500	DEFW0025H	C
8F6B F769	DEFW69F7H	S
8F6D ED6D	DEFW6DEDH	D
8F6F 4312	DEFW1243H	C
8F71 AB5B	DEFW5BABH	S
8F73 AA5B	DEFW5BAAH	D
8F75 3B00	DEFW003BH	C
8F77 B457	DEFW57B4H	S
8F79 6F5B	DEFW5B6FH	D

In addition to changing the RST 3 instructions to RST 5, we must modify the code to setup the RST 5 vector instead of the RST 3 vector.

;Code to setup RST routines

```
;  
-----  
X.8800:    LD    HL,(D$8C82)  
          LD    (X.0010),HL    ; RST 2  
          LD    HL,(D$8C84)  
C.8807 EQU $-2  
          LD    (D.0012),HL  
          LD    HL,(D$8C86)  
C$880E EQU $-1  
          LD    (D.0014),HL  
          LD    A,0C3H        ; JMP instruction  
          LD    (X.0028),A    ; install JMP at RST 5 (revised)  
C$8815 EQU $-2  
          LD    (X.0020),A    ; install JMP at RST 4  
          LD    HL,I$8C87    ; jump address for RST 5  
          LD    (D$0029),HL  ; put it in RST 5 vector (revised)  
          LD    HL,I$8CA6    ; jump address for RST 4  
C$8823:    LD    (D.0021),HL  ; put it in RST 4 vector  
          RET
```

With the changes outlined above, the program appears to run on Heath computers with a Z80 processor. Testing is still in progress to determine if additional changes are required.